

M.Tech. (AI) Lab DL Workstation -- Request for Quotation

Indian Institute of Science is inviting tenders from domestic vendors for supply and installation of DL Workstation at IISc Bangalore [MTech (AI) Lab DL Workstation]

Sl. No.	Technical specifications	Qty.
1.	<p>Processor: Single CPU with minimum 20-Core Intel Xeon E5-2698 v4 2.2 GHz or better</p> <p>CPU Memory: 256GB RDIMM DDR4</p> <p>GPU & memory: 4 x NVIDIA V100 GPUs with 32GB HBM2 per GPU memory. (Total of 128GB GPU memory), All GPUs connected with High bandwidth NVLink interconnect.</p> <p>TFLOPS: (GPU FP16) 450 TF or better</p> <p>Power Requirements: Less than 1800 W</p> <p>Storage: 4 x 1.92 TB SSD</p> <p>System Network: Dual x10 GB Ethernet ports,</p> <p>Display Port: Display ports with 4K resolution (Display will be purchased separately)</p> <p>Desktop/Workstation model with integrated water-cooled system (The advantage of water cooling is that it reduces noise and keeps GPU much cooler.) and should work in ambient temperatures (10°C to 30°C).</p> <p>GPU Communications protocol & Topology: NVLink 2.0 (300GBytes/s) between all the GPUs for efficient application scaling. OS Software: Ubuntu Desktop Host OS</p> <p>Pre-installed and optimized Deep Learning Frameworks: Caffe, CNTK, Tensorflow, Theano, Torch with Docker containers for deploying Deep learning frameworks.</p> <p>Required DL SDKs: CUDA toolkit CUDA tuned Neural Network (cuDNN) Primitives TensorRT Inference Engine DeepStream SDK Video Analytics CUDA tuned BLAS CUDA tuned Sparse Matrix Operations (cuSPARSE) Multi-GPU Communications (NCCL).</p> <p>The minimum 3-years onsite comprehensive warranty should be included.</p>	1 nos.

Instructions to bidders and eligibility criteria

1. Quotations should come only from Indian Original Equipment Manufacturer (OEM) or their authorized Indian distributor. The quotations should be on FOR-IISc Bangalore basis in INR only.

2. Two-cover format

The quotations should be submitted by reputed domestic vendors in a two-cover format comprising a technical part and a commercial part. The envelopes should be sealed and marked clearly as "Envelope No. 1 – Technical Bid" and "Envelope No. 2 – Commercial Bid." The two sealed envelopes must be put in a bigger cover, which should also be sealed and contain the tender number and due date. On the bigger cover should be super-scribed "M.Tech. (AI) Lab DL Workstation." All covers should bear the name, complete postal address, and contact mobile phone/landline number of the bidder.

3. Technical bid

The technical bid should contain (i) the unpriced bill of material with quantities of each line item and (ii) a datasheet for the product/model suggested. Any deviations from the required specifications must be mentioned explicitly. The technical bid should not contain any price information. Non-compliance will lead to a rejection of the bid at the discretion of the technical evaluation committee.

4. Commercial bid

The commercial bid must include the price of the item in Indian currency, indicating installation and commissioning, warranty and maintenance charges, applicable taxes, GST, and total. The prices must be given for each line item separately. Bundling of prices is not acceptable. Installation and commissioning charges, if any, must be quoted as a separate line item -- these will be paid only after successful supply, installation, and acceptance.

5. Warranty terms

- Warranty services for the system supplied by the successful bidder should be valid for a period of three years from the date of acceptance of the equipment.
- During the warranty period, the bidder shall be fully responsible for the manufacturers' warranty in respect of proper design, quality, and workmanship of all the systems supplied.
- During the warranty period, preventive maintenance and repairs of equipment supplied by the bidder are the responsibilities of the bidder. The bidder shall attend to all the hardware problems on site and also replace the defective parts at no extra cost to the purchaser.

6. Evaluation of bids

The technical bid will be evaluated first. Technical bids not meeting the specifications and tender requirements will be disqualified. The commercial bids of technically qualified bidders will be opened subsequently. The date and time of opening the commercial bid will be intimated to the technically qualified bidders. The lowest bid will be identified as the successful bid. In computing the commercial offer, the cost of the equipment, software (if any), installation and commissioning charges, and warranty charges will be included.

7. Additional guidelines

- The equipment must be supplied within four weeks after receiving a firm purchase order from IISc and the installation has to be completed within a week after supply.
- A copy of the masked commercial bid has to be given in the technical offer.
- Delayed or incomplete tenders are liable to be rejected.
- The Technical and Commercial bids should be duly signed (on all pages) by an authorized representative of the bidder.
- Vendors should submit an authorization letter from the OEM.
- The GST at concessional rate and any other statutory levies should be shown separately and not included in the total amount, to enable IISc to avail exemption. IISc is eligible for 5% GST under concessional certificate.
- Offer should be valid for at least 45 days from the date of submission.
- IISc will place the purchase order only on the successful bidder.
- 100% payment will be released after complete delivery and satisfactory installation. Payment will be subject to deduction of TDS as per rules/laws.

Important dates

March 5, 2021: Enquiry letter posted on IISc webpage.

March 12, 2021: Last date for clarifications or queries, to be sent to vijayn@iisc.ac.in.

March 19, 2021: Last date for submission of bids.

An online Pre-bid clarification meeting may be held if required. The bids should be sent to

Chair
Department of Electrical Engineering
Indian Institute of Science
CV Raman Road, Bangalore

before 5.00 PM March 19, 2021. Mention the following reference on the cover: **M.Tech. (AI)
Lab DL Workstation.**