Corrigendum 2:

The specification of the cluster has been revised as:

A HPC Cluster with **1 Head node and at least 16 rackmountable compute nodes** (to give a sustained HPL performance of 40 TF), with a High Performance interconnect .

The specification of the compute nodes, head node, interconnect, and software remain the same as given in the original tender with the clarifications/changes given in the earlier corrigendum (dated 26/11/19).

In the price bid, the bidders should clearly indicate the subcomponent prices (i.e., price of head node, compute nodes, interconnect switch, etc. should be mentioned as separate line items.)

The price of 8GB per core provided in fully-balanced configuration (in lieu of 4 GB per core) (optional item) should be mentioned on a per compute node basis.

The for bid submisison deadline is extended to Friday Dec. 13, 2019, 4.00 p.m.